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U.S. PATENT DOCUMENTS

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/MM/	6,505,207	1/2003	Aggarwal et al.			
	6,122,628	9/2000	Castelli et al.			
	6,192,505	2/2001	Beer et al.			
	6,484,297	11/2002	Dixit et al.			
	6,418,401	7/2002	Dansky et al.			
	6,543,036	4/2003	Iyer et al.			
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	5,566,316	10/1996	Fechner et al.			

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

/MM/		IBM Technical Disclosure Bulletin, Vol. 19, No. 11, April 1977, RANDOM DELAY SIMULATION, Savkar, pages 4257-4258.
/MM/		Jess et al., STATISTICAL TIMING FOR PARAMETRIC YIELD PREDICTION OF DIGITAL INTEGRATED CIRCUITS, DAC 2003, June 2-6, 2003, pages 932-937.

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/MM/			LI et al., PROCESS VARIATION DIMENSION REDUCTION BASED ON SVD, 2003 IEEE, pages IV-672-IV-675.

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